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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Complete if Known

Application No.	10/092,051 / 7802
Filing Date	March 04, 2002
First Named Inventor	Stephen M. Douglass
Art Unit	2812
Examiner Name	Stacy Whitmore
Attorney Docket Number	X-923 US

(use as many sheets as necessary)

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Attorney Docket Number

X-923 US

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
JW. JW. JW.		US- 4,758,985	07-19-88	Carter	
		US- 5,142,625	08-25-92	Nakai	
		US- 4,855,669	08-08-89	Mahoney	
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		US- 5,600,845	02-04-97	Gilson	
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3/17/04

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Application / Conf. N .	10/092,051 / 7802
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			Art Unit	2812
			Examiner Name	Stacy Whitmore
			Attorney Docket Number	X-923 US
Sheet	2	of	7	

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SW		SAYFE KIAEI et al., "VLSI DESIGN OF DYNAMICALLY RECONFIGURABLE ARRAY PROCESSOR-DRAP," IEEE, February 1989, pp. 2484-2488, V3.6, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
SW		VASON P. SRINI, "FIELD PROGRAMMABLE GATE ARRAY (FPGA) IMPLEMENTATION OF DIGITAL SYSTEMS: AN ALTERNATIVE TO ASIC," IEEE, May 1991, pp. 309-314, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
SW		G. MAKI et al., "A RECONFIGURABLE DATA PATH PROCESSOR," IEEE, August 1991, pp. 18-4.1 to 18-4.4, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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SW		CHRISTIAN ISELI et al., "BEYOND SUPERSCALER USING FPGA's," IEEE, April 1993, pp. 486-490, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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10/092,051

7802

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First Named Inventor

Stephen M. Douglass

Art Unit

2812

Examiner Name

Stacy Whitmore

Attorney Docket Number

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U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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by A. W. K.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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OTHER - NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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SW		CHRISTIAN ISELI et al., "AC++ COMPILER FOR FPGA CUSTOM EXECUTION UNITS SYNTHESIS," 1995, pp. 173-179, IEEE, 3 Park Avenue, 17th Floor, New York, NY 10016-5997	
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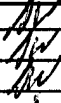
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U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
		US- 6,011,407	01-04-00	New	
		US- 6,172,990	01-09-01	Deb et al.	
		US- 09/991,410	11-16-01	Herron et al.	
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		US- 6,601,227	07-29-03	Trimberger	
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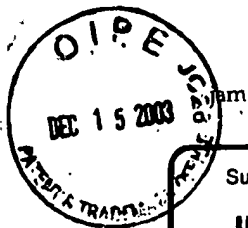
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		Filing Date	March 04, 2002		
		First Named Inventor	Mauna Loa		
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		Examiner Name	Stacy Whitmore		
Sheet	7	of	7	Attorney Docket Number	X-923 US

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[Signature]		US- 6,522,167	02-18-03	Ansari et al.	
		US- 6,434,735	08-13-02	Watkins	
		US- 6,588,006	07-01-03	Watkins	
		US- 6,539,508	03-25-03	Patrie et al.	
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		US- 6,242,945	06-05-01	New	
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